Abstract

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digitally-controlled oscillator (1) comprises input for the supply of a digital input word (100), an adder (10), a stable local oscillator and a delay circuit (11,12), comprising a delay stage (11) with a number of serially-connected coarse delay elements (110) and a fine delay stage (12) with a number of serially-connected fine delay elements (120). The coarse delay stage (11) and the fine delay stage (12) are embodied such that the maximum total delay and the minimum total delay differ by at most one period of the cycle signal. The delay produced by the number of fine delay elements (120) corresponds to the delay of one coarse delay element (11). Each coarse delay element (110) and each fine delay element (120) comprise their own controllable selector (110c;120f;120k).

(Fig. 1)